

Issue Batch No.: H53
Allowed: 06/12/2001
S.N.: 09/088,394

#17M

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: B.S. Beaman et al.

Date: August 20, 2001

Serial No.: 09/088,394

Group Art Unit: 2858

Filed: June 1, 1998

Examiner: V. Nguyen

For: HIGH DENSITY INTEGRATED CIRCUIT APPARATUS, TEST PROBE AND
METHODS OF USE THEREOF

Assistant Commissioner for Patents
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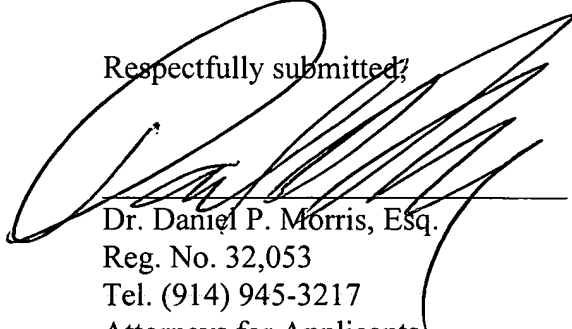
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Director's Office

SUBMISSION OF FORMAL DRAWINGS

Sir:

Enclosed herewith are eleven (11) sheets of formal drawings (FIGS. 1-18) for the
above-identified application, Serial No. 09/088,394 which has a Notice of Allowance date of
June 12, 2001, and Issue Batch Number H53.

Respectfully submitted,



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IBM Docket Number: YO993-028AB

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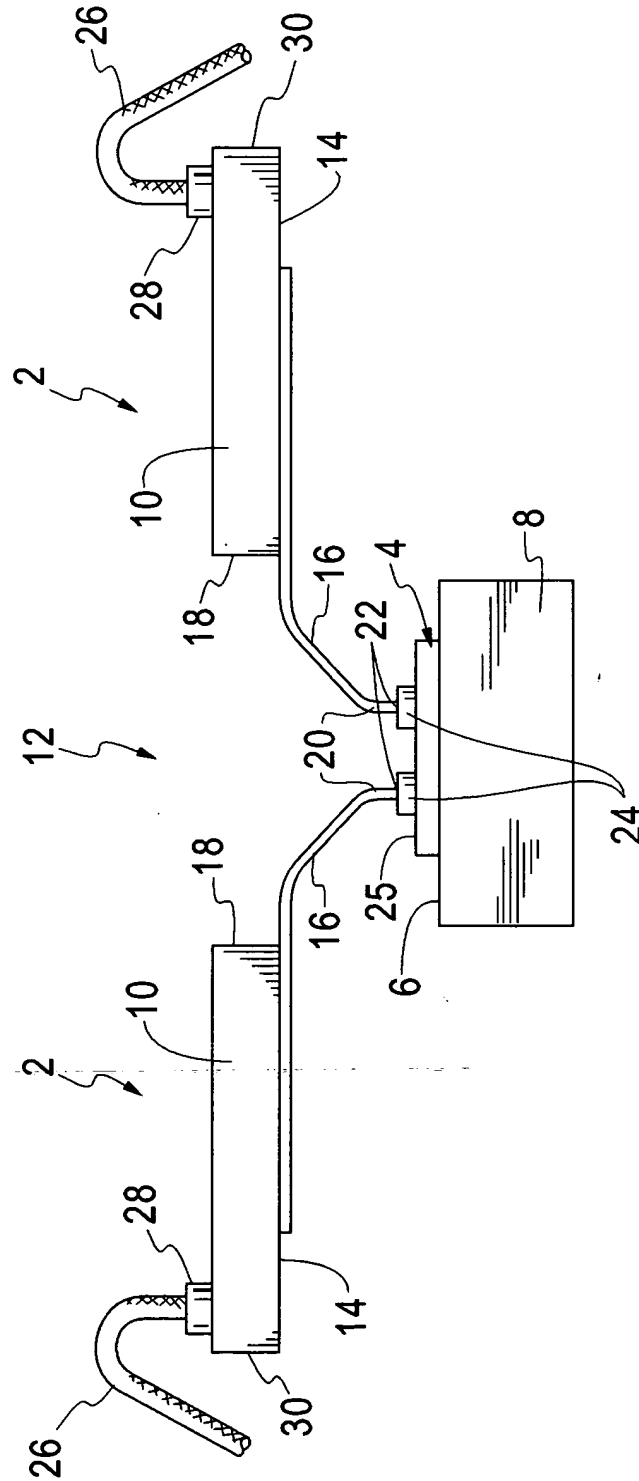


Fig. 1
Prior Art

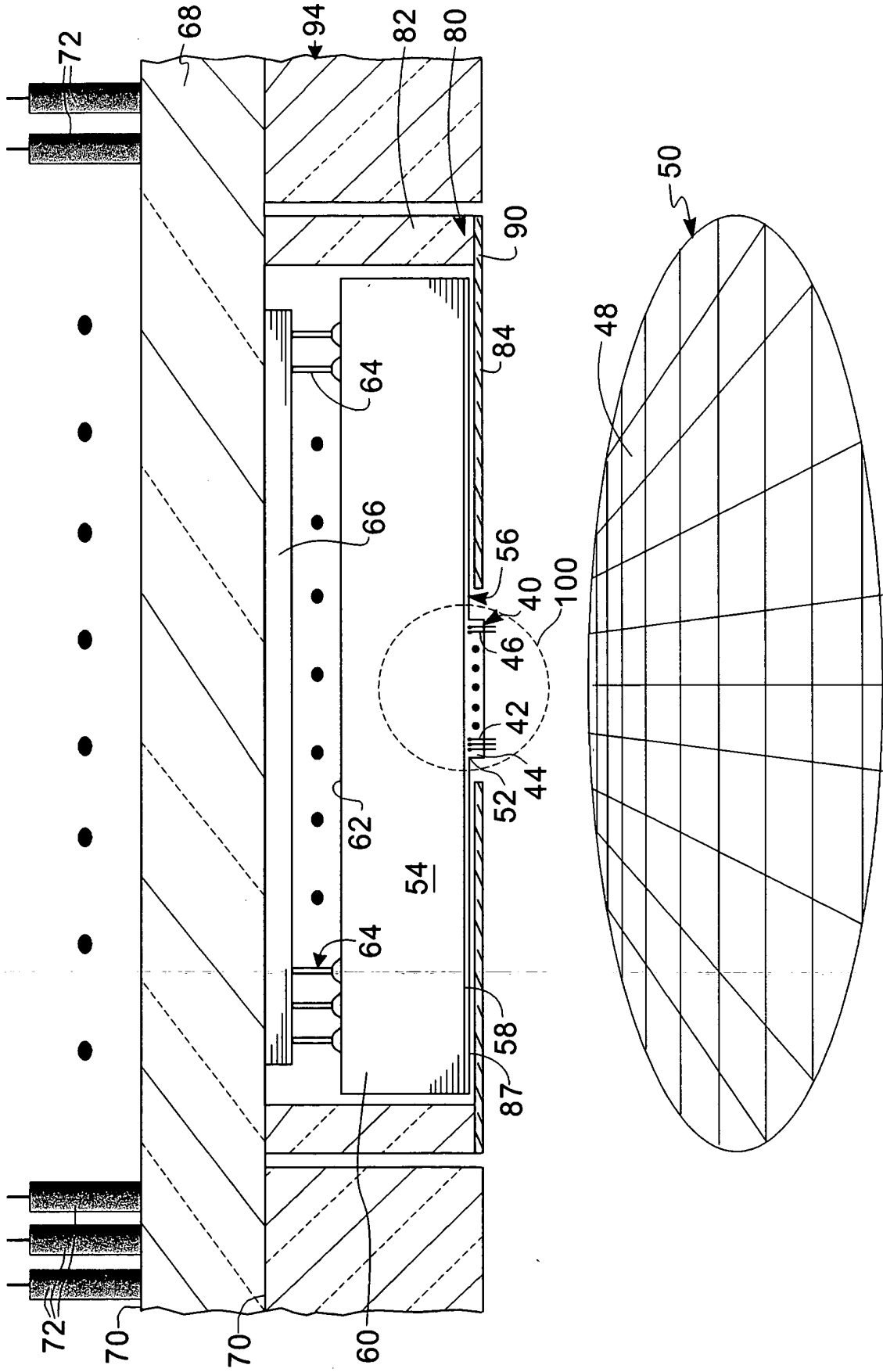


Fig. 2

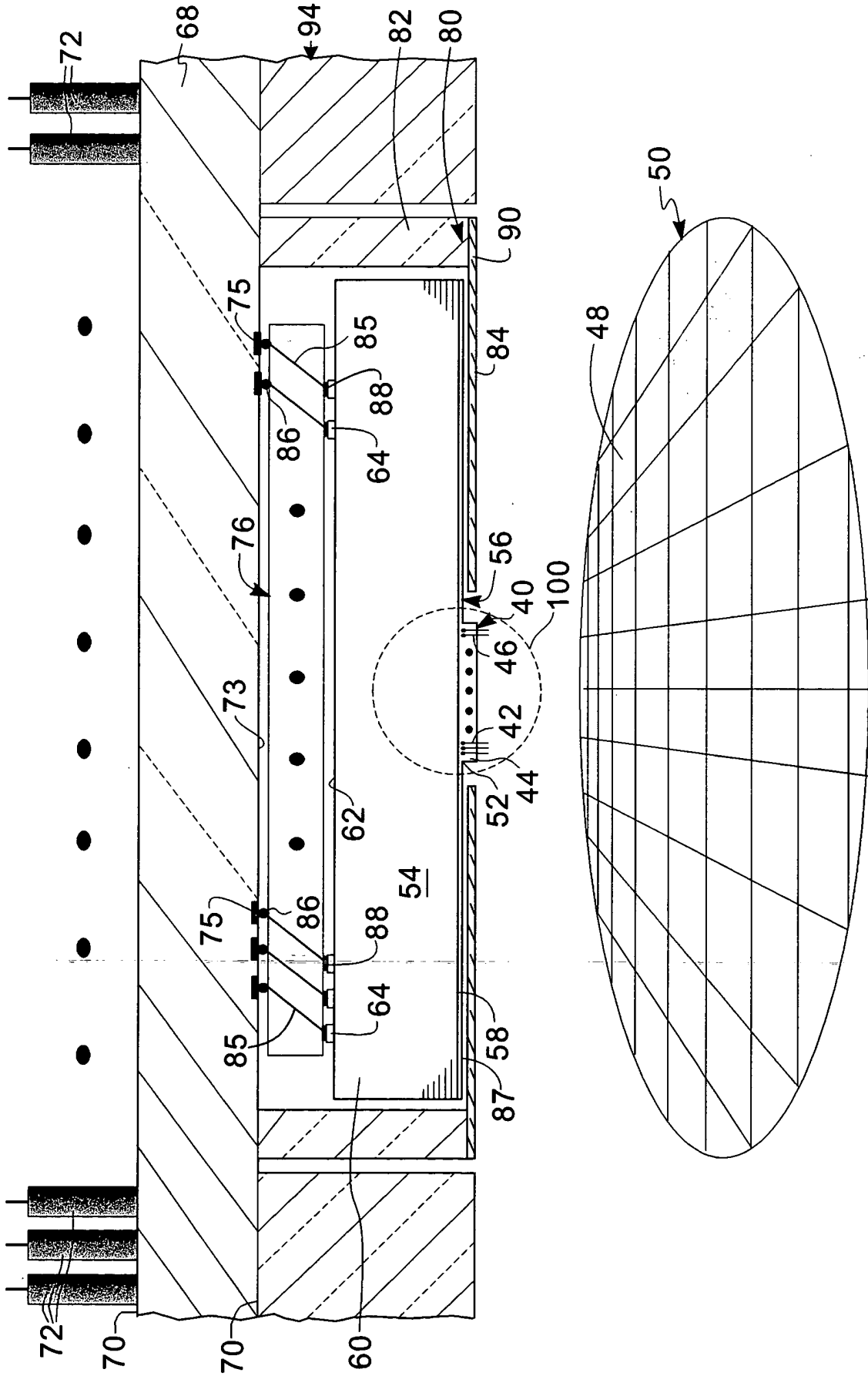


Fig. 3

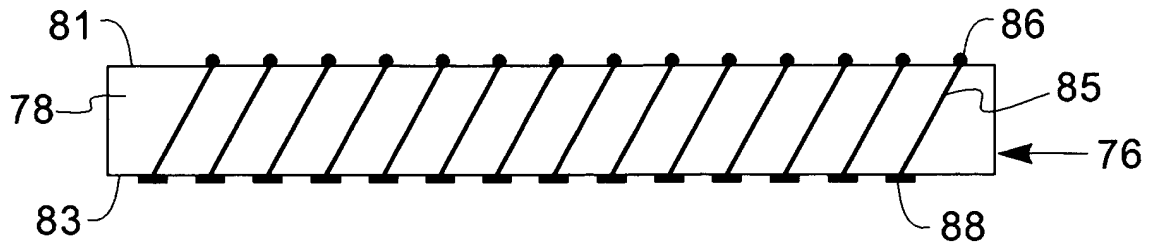


Fig. 4

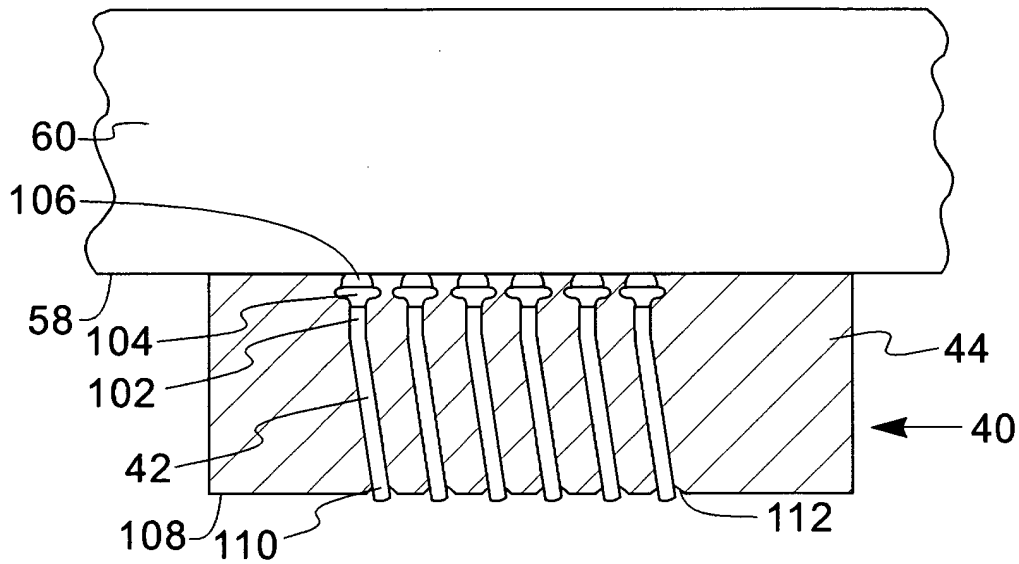


Fig. 5

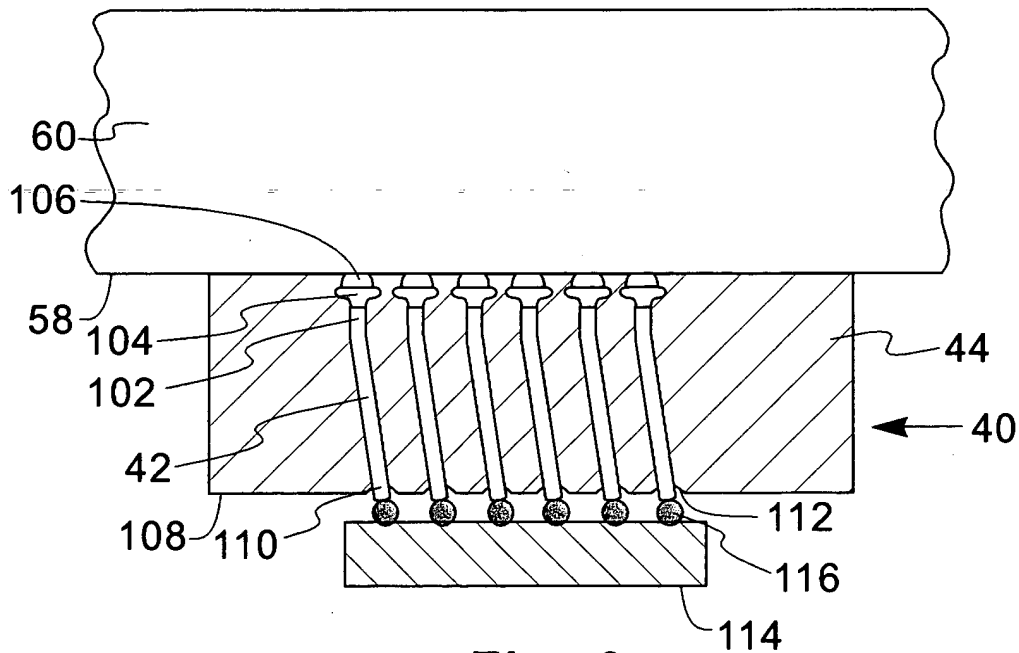


Fig. 6

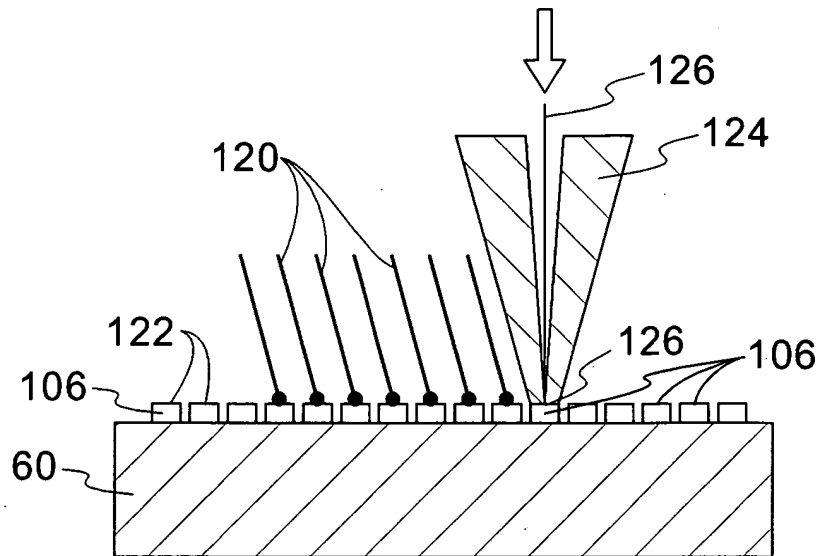


Fig. 7

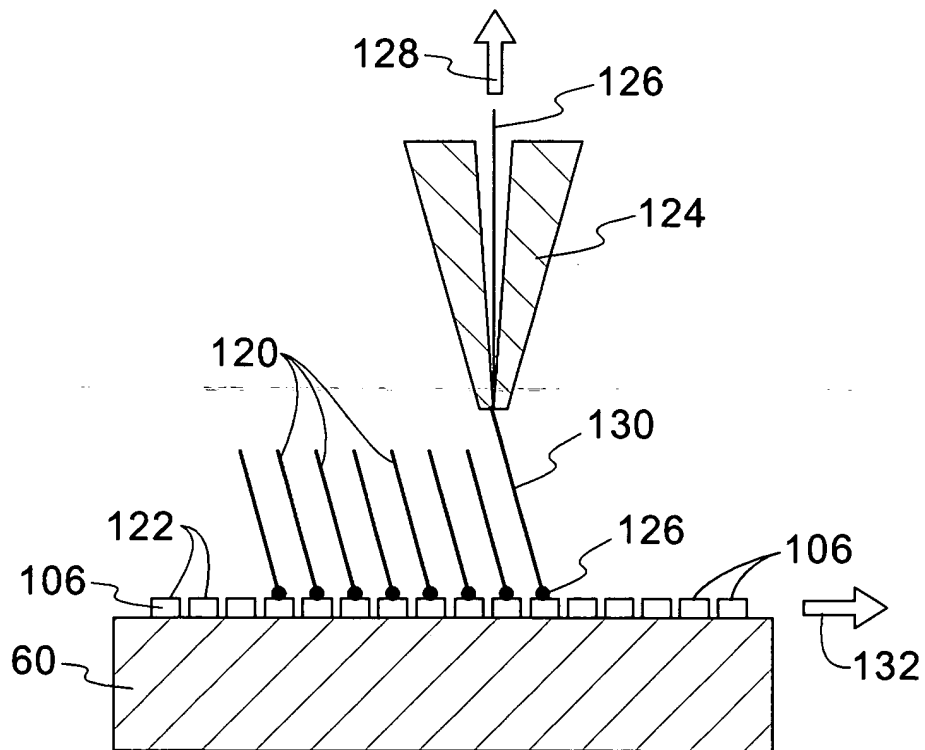


Fig. 8

Fig. 9

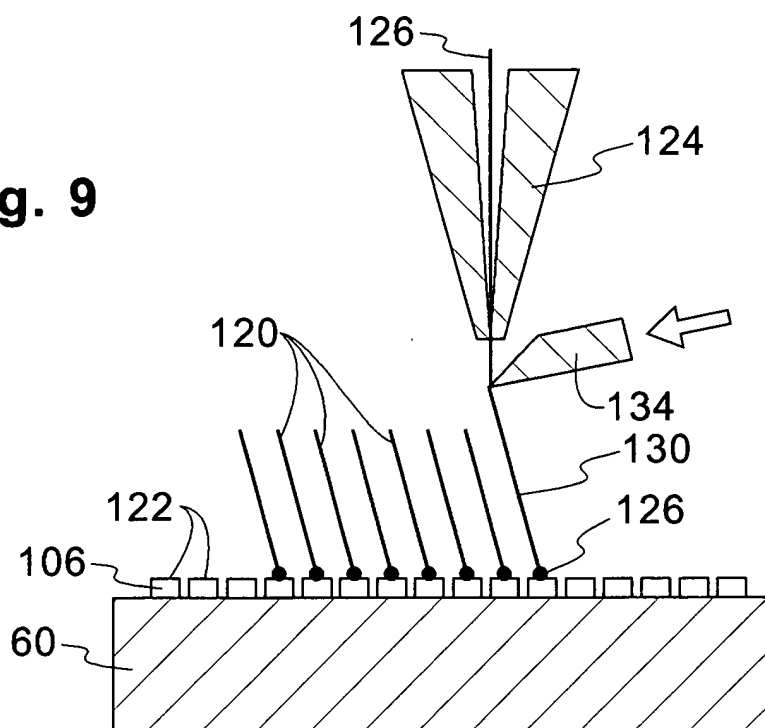
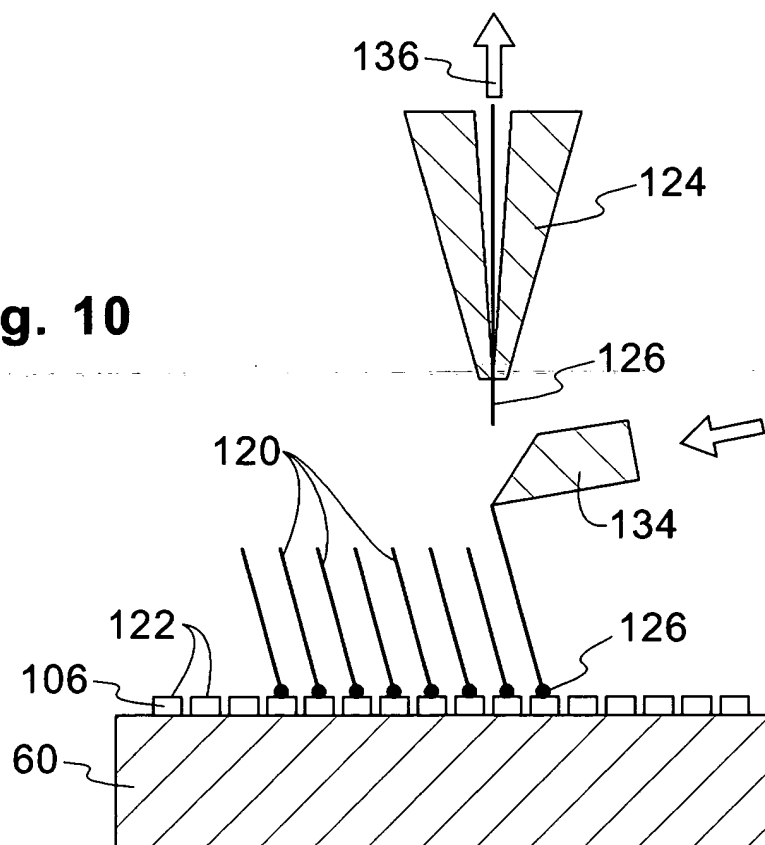


Fig. 10



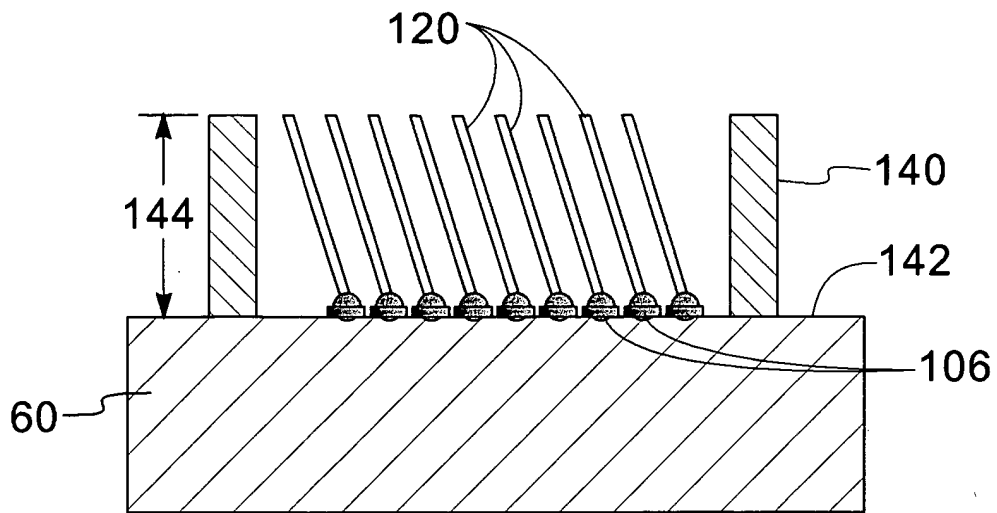


Fig. 11

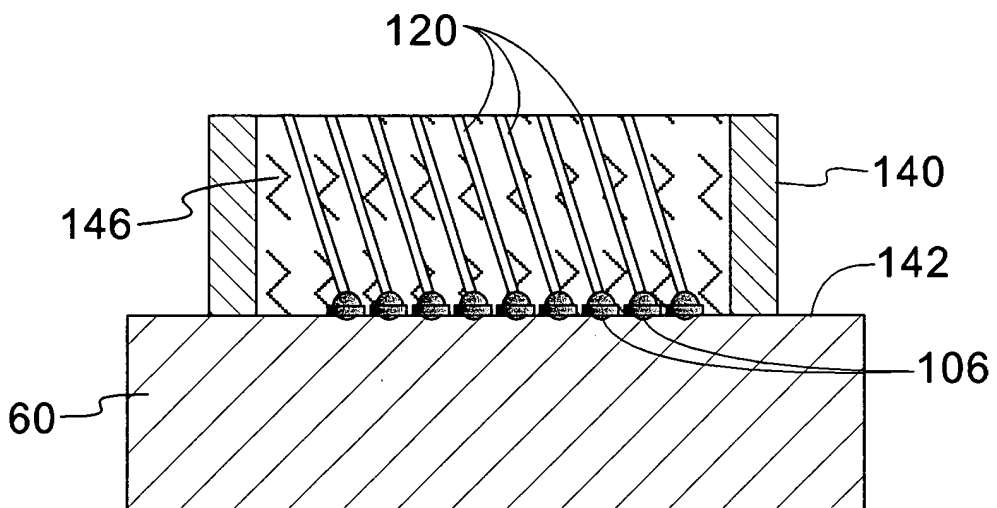


Fig. 12

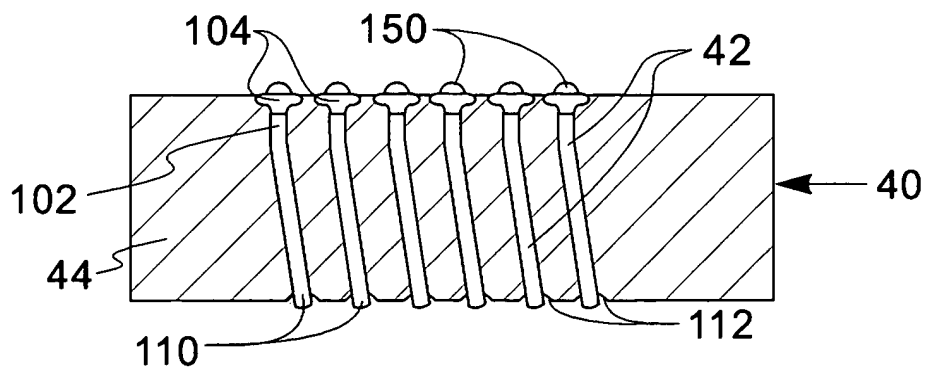


Fig. 13

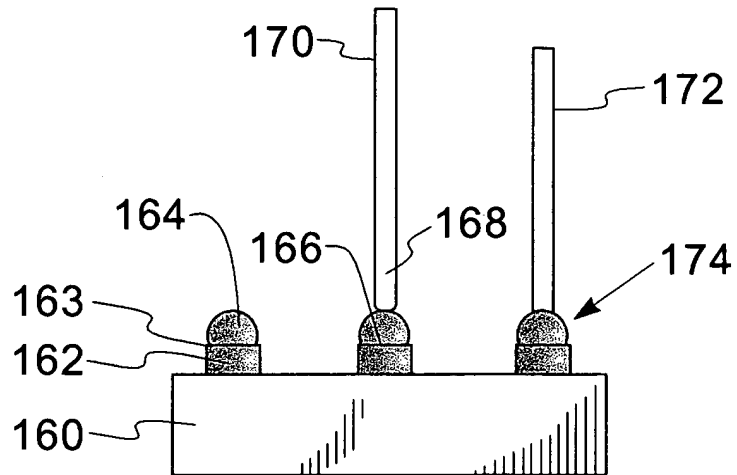


Fig. 14

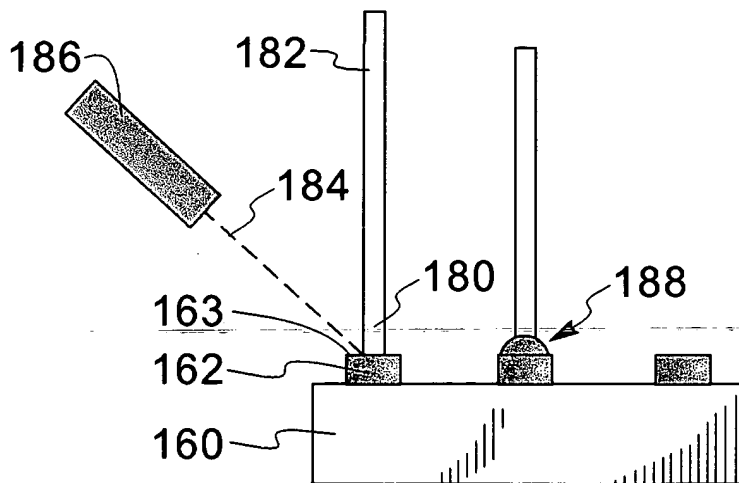


Fig. 15

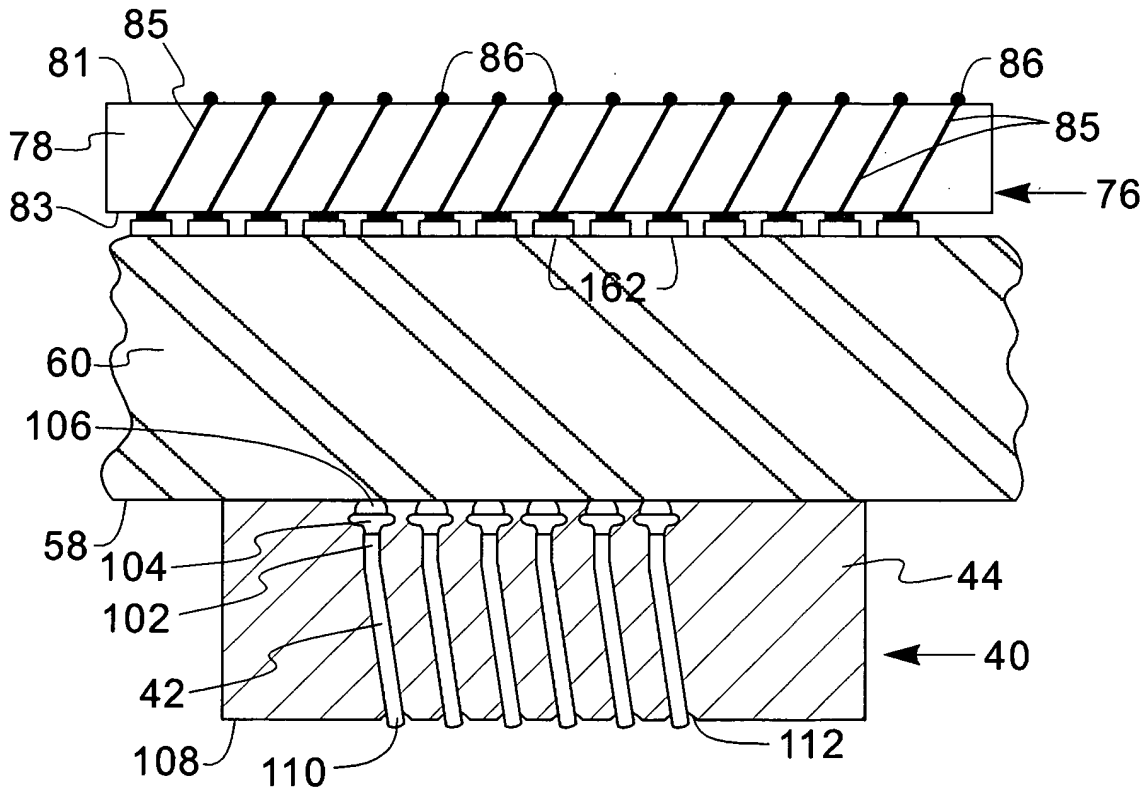
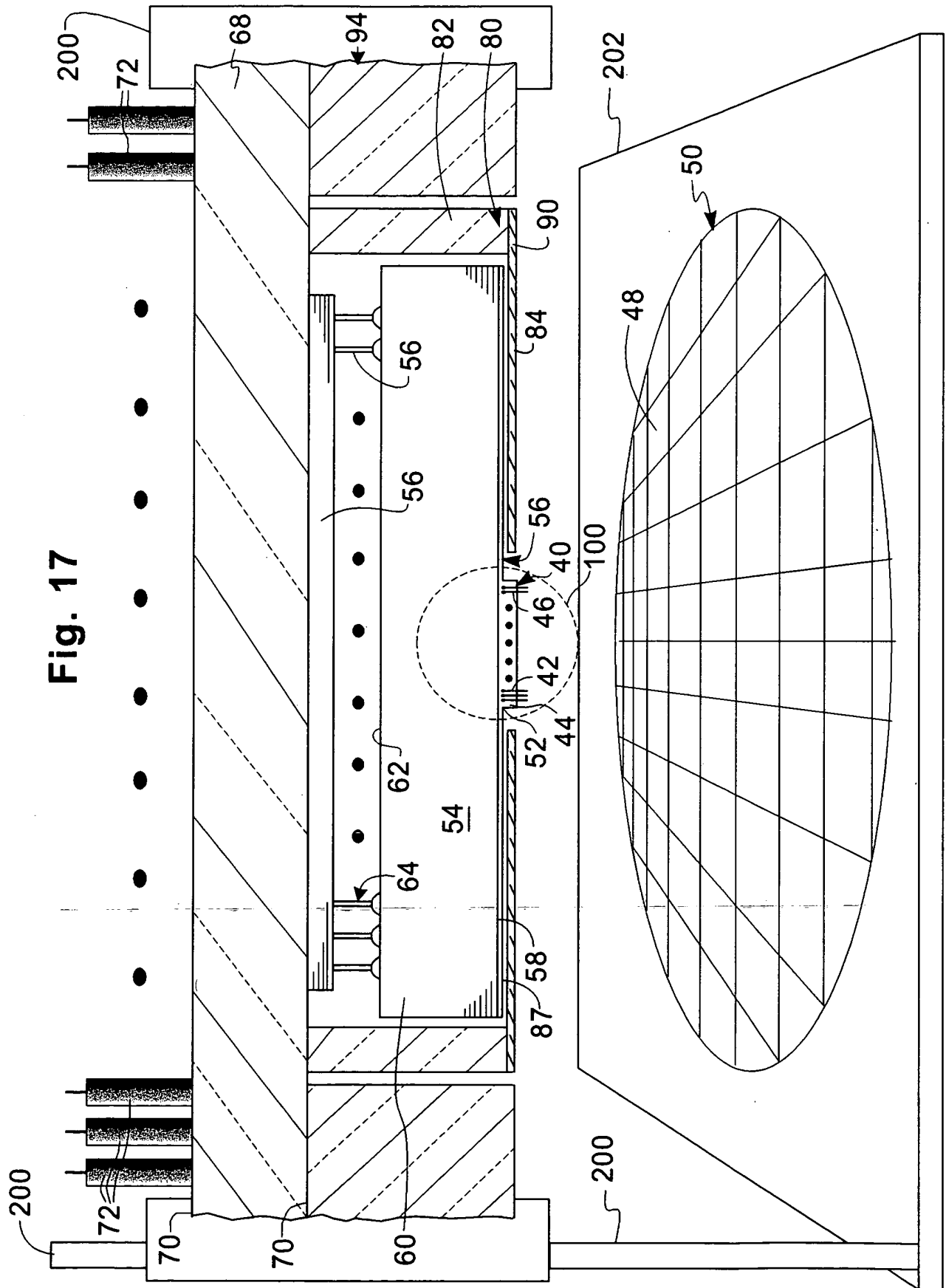


Fig. 16

Fig. 17



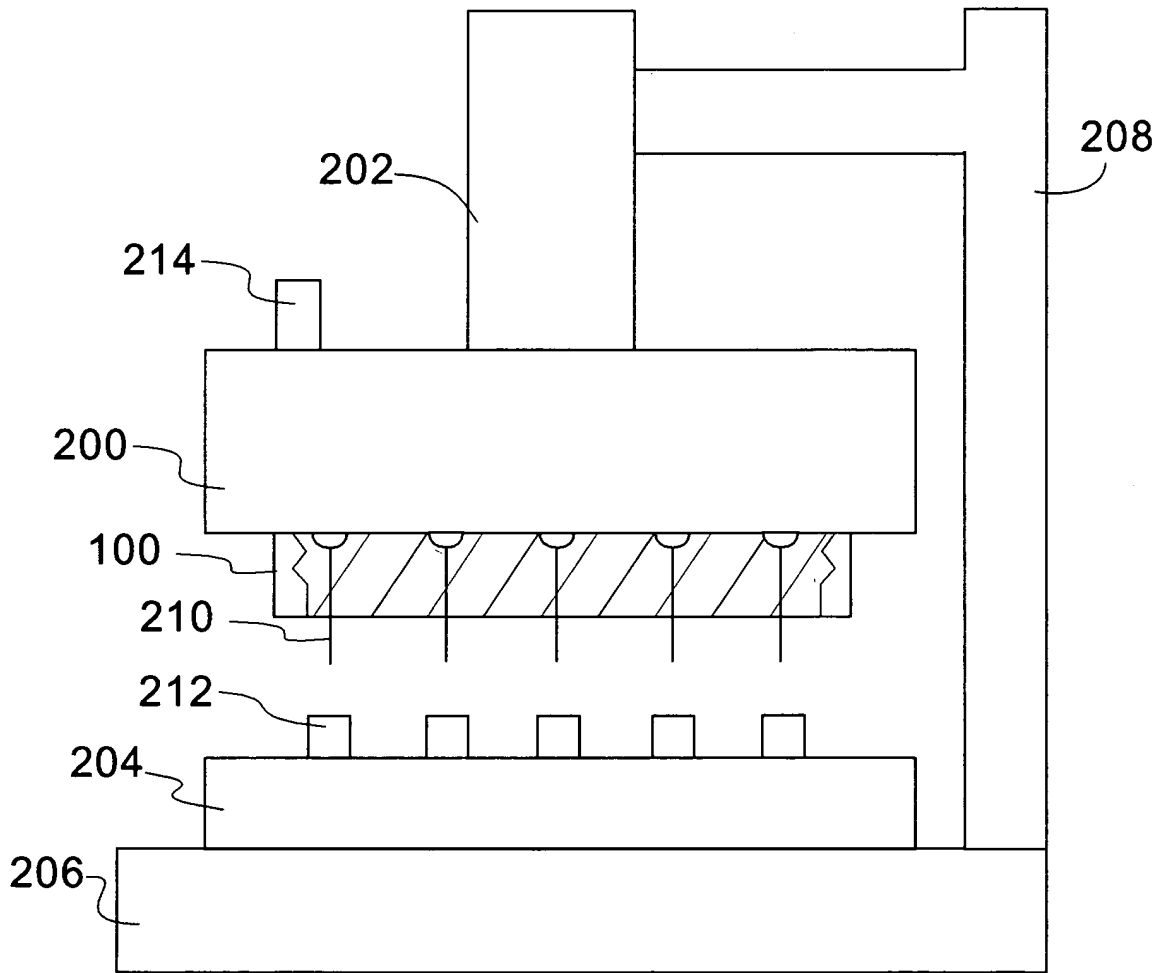


Fig. 18